

WEST Search History

Hide Items

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DATE: Friday, April 16, 2004

Hide? Set Name Query**Hit Count***DB=USPT; PLUR=YES; OP=OR*

<input type="checkbox"/>	L53	5862052.pn.	1
<input type="checkbox"/>	L52	5916306.pn.	1
<input type="checkbox"/>	L51	5918233.pn.	1
<input type="checkbox"/>	L50	6028998.pn.	1
<input type="checkbox"/>	L49	6028998.pn.	1

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

<input type="checkbox"/>	L48	l45 and l46 and L47	2
<input type="checkbox"/>	L47	latch adj object\$1	161
<input type="checkbox"/>	L46	access adj object\$1	4964
<input type="checkbox"/>	L45	hardware adj2 object\$1	599
<input type="checkbox"/>	L44	l23 and L43	0
<input type="checkbox"/>	L43	l41 and L42 and l38 and l18	0
<input type="checkbox"/>	L42	713/100.ccls.	737
<input type="checkbox"/>	L41	713/1-2.ccls.	2009
<input type="checkbox"/>	L40	orchestration adj object\$1	1
<input type="checkbox"/>	L39	l23 and L38	12
<input type="checkbox"/>	L38	719/328.ccls.	669

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<input type="checkbox"/>	L37	6480597.pn.	1
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<input type="checkbox"/>	L35	5691897.pn.	1

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<input type="checkbox"/>	L34	l19 and l32	0
<input type="checkbox"/>	L33	l23 and L32	0
<input type="checkbox"/>	L32	control same latch same (layer\$1 and object\$1)	313
<input type="checkbox"/>	L31	l23 and L30	0
<input type="checkbox"/>	L30	control same latch same (layer\$1 or object\$1)	17730

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DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

<input type="checkbox"/>	L27	l23.ti.	2
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DB=USPT; PLUR=YES; OP=OR

<input type="checkbox"/>	L26	l18 and L25	3
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4-16-04
SPIC ✓*

<input type="checkbox"/>	L25	hal and l23	151
<input type="checkbox"/>	L24	hal or L23	16626
<input type="checkbox"/>	L23	Hardware adj abstraction adj layer\$1	233
<input type="checkbox"/>	L22	wrapper\$1 and L21	0
<input type="checkbox"/>	L21	firmware same l19	28
<input type="checkbox"/>	L20	l5 and L19	0
<input type="checkbox"/>	L19	control adj points	10532
<input type="checkbox"/>	L18	L7 or L8 or L9 or L10 or L11 or L12 or L13 or L14 or L15 or L16 or L17	1702
<input type="checkbox"/>	L17	(719/320).ccls.	75
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<input type="checkbox"/>	L10	(719/313).ccls.	228
<input type="checkbox"/>	L9	(719/312).ccls.	86
<input type="checkbox"/>	L8	(719/311).ccls.	52
<input type="checkbox"/>	L7	(719/310).ccls.	340
<input type="checkbox"/>	L6	L5.ti.	1
<input type="checkbox"/>	L5	latch adj object\$1	40
<input type="checkbox"/>	L4	l1 and l2	0
<input type="checkbox"/>	L3	l1 and l2L2	0
<input type="checkbox"/>	L2	hardware adj control adj object\$1	1
<input type="checkbox"/>	L1	latch adj layer\$1	6

END OF SEARCH HISTORY

First Hit

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L32: Entry 2 of 313

File: PGPB

Oct 10, 2002

PGPUB-DOCUMENT-NUMBER: 20020147854

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020147854 A1

TITLE: Multi-layer software architecture for hardware control

PUBLICATION-DATE: October 10, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Frazier, Brian Edward	Santa Rosa	CA	US	
Sutton, Keith Jeffrey	Santa Rosa	CA	US	
Heyman, Thanh Thien Nguyen	Santa Rosa	CA	US	

APPL-NO: 09/ 825654 [PALM]

DATE FILED: April 4, 2001

INT-CL: [07] G06 F 15/163, G06 F 9/54

US-CL-PUBLISHED: 709/310; 709/311

US-CL-CURRENT: 719/310

REPRESENTATIVE-FIGURES: 1

ABSTRACT:

A software system having a multi-layer architecture for controlling a hardware system including a latch layer, a hardware control layer, an access layer, and an orchestration layer. The latch layer includes a latch object for each of a set of control points of the hardware system. Each latch object provides a common interface in the software system for accessing the corresponding control point. The hardware control layer includes a hardware control object for each of a set of sub-portions of the hardware system. Each hardware control object coordinates accesses to the control points of the corresponding sub-portion through the latch layer. The access layer includes an access object for each of a set of groupings of the sub-portions. Each access object coordinates accesses to the corresponding grouping of the sub-portions. The orchestration layer includes an orchestration object for each of a set of functional features of the hardware system. Each orchestration object provides a common interface in the software system for accessing a corresponding grouping of the access objects which are associated with the corresponding functional feature.

First Hit

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L32: Entry 44 of 313

File: DWPI

Oct 10, 2002

DERWENT-ACC-NO: 2003-219647

DERWENT-WEEK: 200321

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TITLE: Software system for firmware and embedded systems has hardware control objects coordinating accesses to control points of hardware system through common interface provided by latch objects of latch layer

INVENTOR: FRAZIER, B E; HEYMAN, T T N ; SUTTON, K J

PATENT-ASSIGNEE: FRAZIER B E (FRAZI), HEYMAN T T N (HEYMI), SUTTON K J (SUTTI)

PRIORITY-DATA: 2001US-0825654 (April 4, 2001)

Search Selected

Search ALL

Clear

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
<input type="checkbox"/> <u>US 20020147854 A1</u>	October 10, 2002		007	G06F015/163

APPLICATION-DATA:

PUB-NO	APPL-DATE	APPL-NO	DESCRIPTOR
US20020147854A1	April 4, 2001	2001US-0825654	

INT-CL (IPC): G06 F 9/54; G06 F 15/163

ABSTRACTED-PUB-NO: US20020147854A

BASIC-ABSTRACT:

NOVELTY - A latch object (40) of a latch layer (500) provides a common interface in the software system for accessing corresponding control points of the hardware system. Each hardware control object (30) of the hardware control layer (510) coordinates accesses to the control points of the corresponding sub portion through the latch layer.

USE - Used in firmware systems such as embedded systems.

ADVANTAGE - Provides clarity and consistency in generating new control algorithms for a system as well as maintenance of a system and enables implementation of high level, texture based algorithm which require little knowledge of underlying hardware system.

DESCRIPTION OF DRAWING(S) - The figure shows the software system.

hardware control object 30

Latch object 40

ABSTRACTED-PUB-NO: US20020147854A

EQUIVALENT-ABSTRACTS:

Record Display Form

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CHOSEN-DRAWING: Dwg. 1/4

DERWENT-CLASS: T01

EPI-CODES: T01-F01B; T01-J20B;